

8" SVGA High brightness color TFT-LCD module

AGL model code : VM08B1 V8

Date: May. 30th, 2022

Version: 01

Note: This specification is subject to change without notice

Customer :

Approved by :

Date :

Approved	Prepared
Date:	Date:



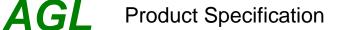


AGL Product Specification Applied Green Light, Inc.

RECORD OF REVISION

Vers	sion and Date	Page	Old description	New description	Remark
0.1	2012/05/22	All	First Edition for customer		
0.2	2014/07/09	9	3.1.3. Backlight Driving Conditions- Item Values Veltage for LED backlight Vietage for LED backlight bc 21.5. V. Note 1 Current for LED backlight bc 360. mAr. - LED life time. - 1 RD - He. Note 2 T TS Carent for LED Supply Voltage is defined by the number of LED at Ta=25°C at 11. - Storms. - Note 2: The "LED Bind him" is defined as the module brightness decrease to 50% reginal beginters at Ta=25°C at 11. - Note 2: The "LED it function" is defined as the module brightness decrease to 100%.	$\label{eq:response} \begin{array}{ c c c c c c } \hline 1.1 Backlight Deving Conditions. \\ \hline $$ $$ $$ $$ $$ $$ $$ $$ $$ $$ $$ $$ $$	
0.3	2022/05/30	4	Backlight power: 4.752W	Backlight power: 2.88W	
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1. General Specifications

No.	Item	Specification	Remark
1	LCD size	8.0 inch(Diagonal)	
2	Driver element	a-Si TFT active matrix	
3	Resolution	$800 \times 3(RGB) \times 600$	
4	Display mode Normally White, Transmissive		
5	Dot pitch	$0.0675(W) \times 0.2025(H) mm$	
6	Active area	162.0(W) × 121.5(H) mm	
7	Module size	$183.0(W) \times 141.0(H) \times 5.6(D) mm$	Note 1
8	Surface treatment	Anti-Glare	
9	Color arrangement	RGB-stripe	
10	Interface	Digital	
11	Backlight power consumption	2.88W (Typ.)	
12	Panel power consumption	0.356W (Typ.)	
13	Weight	258g (Typ.)	

Note 1: Refer to Mechanical Drawing.



AGL Product Specification

2. Pin Assignment

2.1. TFT LCD Panel Driving Section

- 1. FPC Connector is used for the module electronics interface. The recommended model is FH12A-50S-0.5SH manufactured by Hirose.
- 2. LED Light Bar Connector is used for the integral backlight system. The recommended model is BHSR-02VS-1 manufactured by JST.

Pin No	Symbol	I/O	Function	Remark
1	NC		No connection	
2	NC		No connection	
3	NC		No connection	
4	NC		No connection	
5	GND	Р	Power ground	
6	VCOM	Ι	Common voltage	
7	Vcc	Р	Power for Digital circuit	
8	MODE	Ι	DE/SYNC mode select	Note 3
9	DE	Ι	Data Input Enable	
10	VS	Ι	Vertical Sync Input	
11	HS	Ι	Horizontal Sync Input	
12	B7	Ι	Blue data (MSB)	
13	B6	Ι	Blue data	
14	B5	Ι	Blue data	
15	B4	Ι	Blue data	
16	B3	Ι	Blue data	
17	B2	Ι	Blue data	
18	B1	Ι	Blue data	
19	B0	Ι	Blue data (LSB)	
20	G7	Ι	Green data (MSB)	
21	G6	Ι	Green data	
22	G5	Ι	Green data	
23	G4	Ι	Green data	
24	G3	Ι	Green data	
25	G2	Ι	Green data	
26	G1	Ι	Green data	

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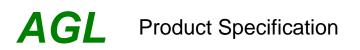
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27	G0	Ι	Green data (LSB)	
28	R7	Ι	Red data (MSB)	
29	R6	Ι	Red data	
30	R5	Ι	Red data	
31	R4	Ι	Red data	
32	R3	Ι	Red data	
33	R2	Ι	Red data	
34	R1	Ι	Red data	
35	R0	Ι	Red data (LSB)	
36	GND	Р	Power ground	
37	DCLK	Ι	Sample clock	
38	GND	Р	Power ground	
39	L/R	Ι	Right/Left selection	Note 2,5
40	U/D	Ι	Up/down selection	Note 2,5
41	Vgh	Р	Gate ON voltage	
42	VGL	Р	Gate OFF voltage	
43	AVdd	Р	Power for Analog circuit	
44	RESET	Ι	Global reset pin.	Note 1
45	NC		No connection	
46	Vсом	Ι	Common voltage	
47	DITHB	Ι	Dithering function	Note 4
48	GND	Р	Power ground	
49	NC		No connection	
50	NC		No connection	

I: input, O: output, P: Power

- Note 1: Global reset pin. Active Low to enter Reset State. Suggest to connecting with an RC reset circuit for stability. Normally pull high.
- Note 2: Selection of scanning mode

Setting of scan control input		Scanning direction		
U/D	R/L	Scanning direction		
GND	Vcc	Up to down, left to right		
Vcc	GND	Down to up, right to left		
GND	GND	Up to down, right to left		
Vcc Vcc		Down to up, left to right		





Note 3: DE/SYNC mode select, Normally pull high.

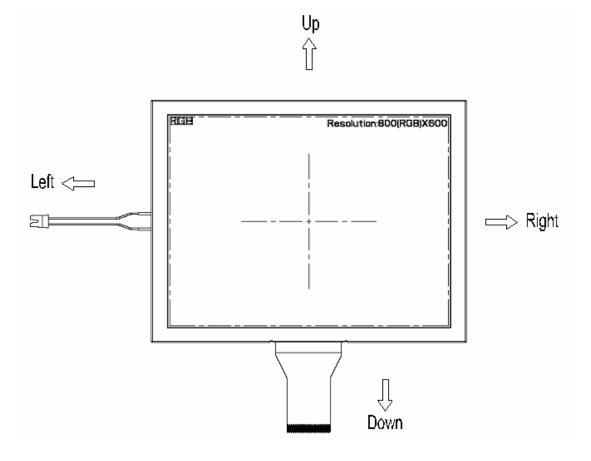
H: DE mode.

L: HS/VS mode.

Note4: Dithering function enable control. Normally pull high.

- DITHB="1",Disable internal dithering function. For 18bit RGB interface, connect two LSB bits of all the R/G/B data buses to GND.
- DITHB="0",Enable internal dithering function, For TTL 24bit parallel RGB image data input.

Note 5: Definition of scanning direction. Refer to the figure as below:



2.2. Backlight Unit Section

Pin No.	Symbol	I/O	Function	Remark
1	$V_{\text{LED}+}$	Р	Power for LED backlight anode	Red
2	VLED-	Р	Power for LED backlight cathode	Black

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3. Operation Specifications

3.1. Absolute Maximum Rating

		(GNL	AVss=0	, Note I)
Item	Symbol	Va	lues	Unit	Remark
Item	Symbol	Min.	Max.	Umt	Kelliark
	Vcc	-0.3	5.0	V	
	AVdd	-0.5	13.5	V	
Power voltage	$V_{ m GH}$	13.0	19.0	V	
	V_{GL}	-12.0	-2.0	V	
	V_{GH} - V_{GL}	-	31.0	V	
Operation temp.	Тор	-20	70	°C	
Storage temp.	Тѕт	-30	80	°C	
LED Forward Current	I_{F}	-	120	mA	each LED

(GND=AVss=0V, Note 1)

Note 1: The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

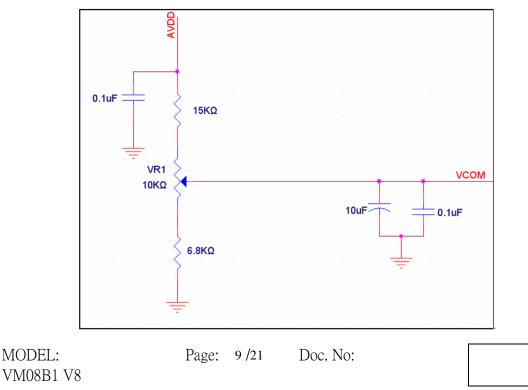


3.1.1 Typical Operation Condition

	-		(GNI	$D = AV_{SS} = 0$	V, Note	1)
14	6h - l		Values	T T . •4	. .	
Item	Symbol	Min.	Тур.	Max.	Unit	Remark
	V _{CC}	3.0	3.3	3.6	V	Note 2
Power voltage	AV_{DD}	10.2	10.4	10.6	V	
	V _{GH}	15.3	16.0	16.7	V	
	V _{GL}	-7.7	-7.0	-6.3	V	
Input signal voltage	V _{COM}	2.8	(3.8)	4.8	V	Note 4
Input logic high voltage	V _{IH}	$0.7 V_{CC}$	-	V _{CC}	V	Note 3
Input logic low voltage	V _{IL}	0	-	$0.3 V_{CC}$	V	INOLE 5

Note 1: Be sure to apply V_{CC} and V_{GL} to the LCD first, and then apply V_{GH} .

- Note 2: V_{CC} setting should match the signals output voltage (refer to Note 3) of customer's system board .
- Note 3: DCLK,HS,VS,RSTB,UPDN,STLR,MODE,DITHB.
- Note 4. Typical Vcom is only a reference value, it must be optimized according to each LCM. Be sure to use VR



3.1.2 Current Consumption

	(GND=AV _{SS} =0)						
Item	Symbol		Values		Unit	Remark	
	Symbol	Min.	Тур.	Max.	Unit		
Current for Driver	I_{GH}	-	0.2	0.5	mA	$V_{GH} = 16.0 V$	
	I _{GL}	-	0.2	1.0	mA	$V_{GL} = -7.0V$	
	I _{CC}	-	5.5	10.0	mA	V _{CC} =3.3V	
	IAV _{DD}	-	32.0	50.0	mA	$AV_{DD} = 10.4V$	

3.1.3. Backlight Driving Conditions

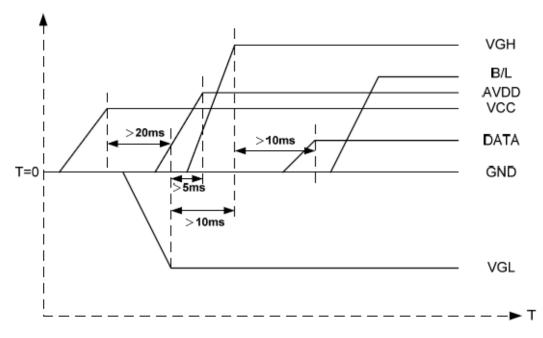
Itom	Symbol		Values		Unit	Remark
Item	Symbol	Min.	Тур.	Max.	Umt	
Voltage for LED backlight	$V_{\rm L}$		18		V	Note 1
Current for LED backlight	IL		160		mA	
LED life time	-		100K	-	Hr	Note 2

- Note 1: The LED Supply Voltage is defined by the number of LED at Ta=25°C and IL =160mA.
- Note 2: The "LED life time" is defined as the module brightness decrease to 50% original brightness at Ta=25°C and IL =160mA. The LED lifetime could be decreased if operating IL is lager than 160 mA.

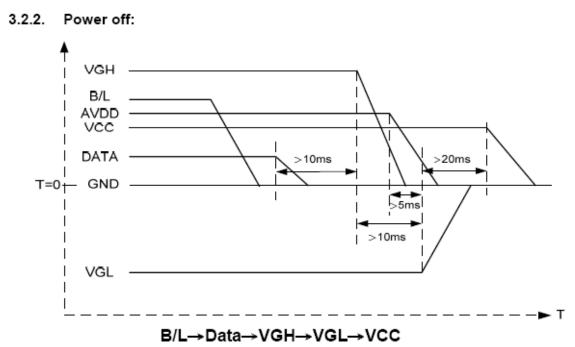


3.2 Power Sequence

3.2.1. Power on:



VCC→VGL→VGH→Data→B/L



Note: Data include R0~R5, B0~B5, GO~G5, STLR, UPDN, DCLK, HS, VS, DE.

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3.3 Timing Characteristics

3.3.1 AC Electrical Characteristics

	G I I	Valu	ies	T T •4		
Item	Symbol	Min.	Тур.	Max.	Unit	Remark
HS setup time	Thst	8	-	-	Ns	
HS hold time	Thhd	8	-	-	Ns	
VS setup time	Tvst	8	-	-	Ns	
VS hold time	Tvhd	8	-	-	Ns	
Data setup time	Tdsu	8	-	-	Ns	
Data hole time	Tdhd	8	-	-	Ns	
DE setup time	Tesu	8	-	-	Ns	
DE hole time	Tehd	8	-	-	Ns	
VDD Power On Slew rate	Tpor	-	-	20	ms	
RSTB pulse width	TRst	10	-	-	us	
CLKIN cycle time	Tcoh	20	-	-	Ns	
CLKIN pulse duty	Tcwh	40	50	60	%	
Output stable time	Tsst	-	-	6	us	





3.3.2 Timing

Item	S-mah al		Values	Unit	Remark	
Item	Symbol	Min	Тур	Max	Umt	Kemai K
Horizontal Display Area	Thd	-	800	-	DCLK	
DCLK Frequency	Fclk	-	40	50	MHz	
One Horizontal Line	Th	862	1056	1200	DCLK	
HS pulse width	Thpw	1	-	40	DCLK	
HS Back Porch(Blanking)	Thb	46	46	46	DCLK	
HS Front Porch	thfp	16	210	354	DCLK	

Itom	Grouphal		Values	T	Remark	
Item	Symbol	Min	Тур	Max	Unit	Kemark
Vertical Display Area	tvd	-	600	-	TH	
VS period time	tv	624	635	700	TH	
VS pulse width	tvpw	1	-	20	TH	
VS Back Porch(Blanking)	tvb	23	23	23	TH	
VS Front Porch	tvfp	1	12	77	TH	





3.3.3. Timing Diagram

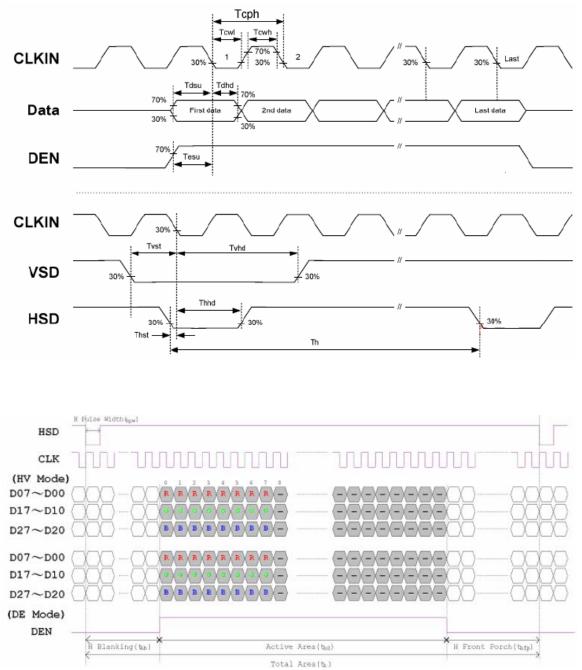


Figure 3.2 Horizontal input timing diagram.



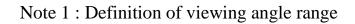
4. Optical Specifications

Itom	Sembol Condition			Values	Unit	Remark	
Item	Symbol	Condition	Min.	Тур.	Max.	Unit	кешагк
	θ_L	$\Phi=180^{\circ}(9 \text{ o'clock})$	60	70	-		Note 1
View angle	θr	$\Phi=0^{\circ}(3 \text{ o'clock})$	60	70	-	degree	
(CR≥10)	θт	$\Phi=90^{\circ}(12 \text{ o'clock})$	40	50	-		
	θв	$\Phi=270^{\circ}(6 \text{ o'clock})$	60	70	-		
Response	Ton		-	10	20	msec	Note 3
	Toff		-	15	30	msec	Note 3
Contrast ratio	CR	Normal θ=Φ=0°	400	500	-	-	Note 4
Color chromaticity	Wx		0.26	0.31	0.36	-	Note 2 Note 5 Note 6
	Wy		0.28	0.33	0.38	-	
Luminance			800	1000	-	Cd/m ²	Note 6
Uniformity			-	70	-	%	Note 7

Test Conditions:

- 1. Vcc=3.3V, IL=480mA (Backlight current), the ambient temperature is 25°C.
- 2. The test systems refer to Note 2.





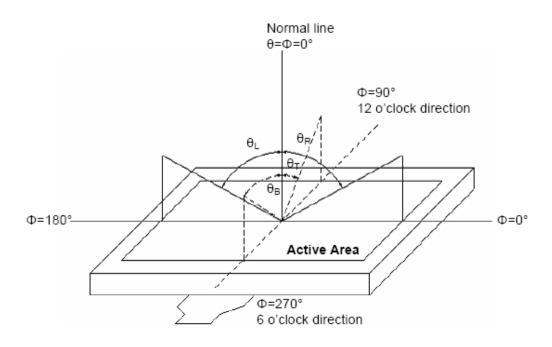


Fig. 4.1 Definition of viewing angle

Note 2: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 30 minutes operation, the optical properties are measured at the center point of the LCD screen. (Response time is measured by Photo detector TOPCON BM-7, other items are measured by BM-5A/Field of view: 1° /Height: 500mm.)

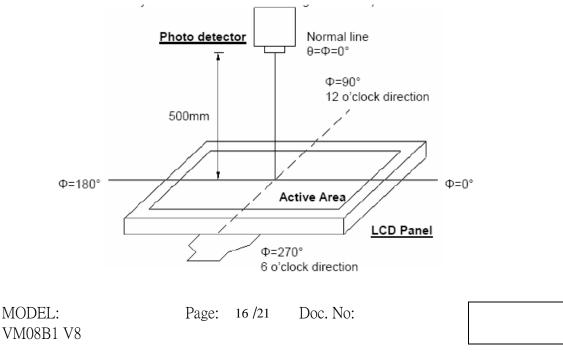
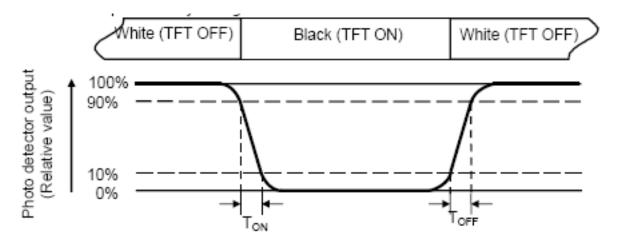




Fig. 4.2 Optical measurement system setup

Note 3: Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%.



Note 4: Definition of Contrast ratio

Contrast ratio (CR) = $\frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$

Note 5: Definition of color chromaticity (CIE1931) Color coordinates measured at center point of LCD.

Note 6: All input terminals LCD panel must be ground while measuring the center area of the panel. The LED driving condition is IL=180mA .





Note 7: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer to Fig. 4.4). Every measuring point is placed at the center of each measuring area.

Luminance Uniformity
$$(Yu) = \frac{B_{min}}{B_{max}}$$

L ----- Active area length W ----- Active area width

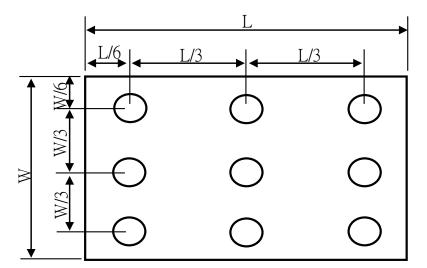


Fig. 4.4 Definition of measuring points

B_{max}: The measured maximum luminance of all measurement position. B_{min}: The measured minimum luminance of all measurement position.

5. Reliability Test Items

Item	Test Conditions	Remark
High Temperature Storage	$Ta = 80^{\circ}C$ 240hrs	Note 1, 4
Low Temperature Storage	$Ta = -30^{\circ}C$ 240hrs	Note 1, 4
High Temperature Operation	$Ts = 70^{\circ}C$ 240hrs	Note 2, 4
Low Temperature Operation	$Ta = -20^{\circ}C$ 240hrs	Note 1, 4
Operate at High Temperature and Humidity	+40°C, 90%RH 240hrs	Note 4
Thermal Shock	-30° C/30 min ~ $+80^{\circ}$ C/30 min for a total 100 cycles, Start with cold temperature and end with high temperature.	Note 4
Vibration Test	Frequency range:10~55Hz Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2 hours for each direction of X. Y. Z. (6 hours for total)	
Mechanical Shock	100G 6ms, $\pm X$, $\pm Y$, $\pm Z$ 3 times for each direction	
Electro Static Discharge	± 2 KV, Human Body Mode, 100pF/1500 Ω	

- Note 1: Ta is the ambient temperature of samples.
- Note 2: Ts is the temperature of panel's surface.
- Note 3: In the standard condition, there shall be no practical problem that may affect the display function. After the reliability test, the product only guarantees operation, but don't guarantee all of the cosmetic specification.
- Note 4: Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.



6. General Precautions

6.1. Safety

Liquid crystal is poisonous. Do not put it in your mouth. If liquid crystal touches your skin or clothes, wash it off immediately by using soap and water.

6.2. Handling

- 1. The LCD panel is plate glass. Do not subject the panel to mechanical shock or to excessive force on its surface.
- 2. The polarizer attached to the display is easily damaged. Please handle it carefully to avoid scratch or other damages.
- 3. To avoid contamination on the display surface, do not touch the module surface with bare hands.
- 4. Keep a space so that the LCD panels do not touch other components.
- 5. Put cover board such as acrylic board on the surface of LCD panel to protect panel from damages.
- 6. Transparent electrodes may be disconnected if you use the LCD panel under environmental conditions where the condensation of dew occurs.
- 7. Do not leave module in direct sunlight to avoid malfunction of the ICs.

6.3. Static Electricity

- 1. Be sure to ground module before turning on power or operating module.
- 2. Do not apply voltage which exceeds the absolute maximum rating value.

6.4. Storage

- 1. Store the module in a dark room where must keep at 25±10°C and 65%RH or less.
- 2. Do not store the module in surroundings containing organic solvent or corrosive gas.
- 3. Store the module in an anti-electrostatic container or bag.

6.5. Cleaning

- 1. Do not wipe the polarizer with dry cloth. It might cause scratch.
- 2. Only use a soft sloth with IPA to wipe the polarizer, other chemicals might permanent damage to the polarizer.

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7. Mechanical Drawing

